

FACSIMILE DEVICE ON SENDER SIDE (SENDING DEVICE): 111 CALL	GATEWAY DEVICE ON SENDER SIDE : 211	GATEWAY DEVICE ON RECEIVER SIDE:221	FACSIMILE DEVICE ON RECEIVER SIDE (RECEIVING DEVICE): 121

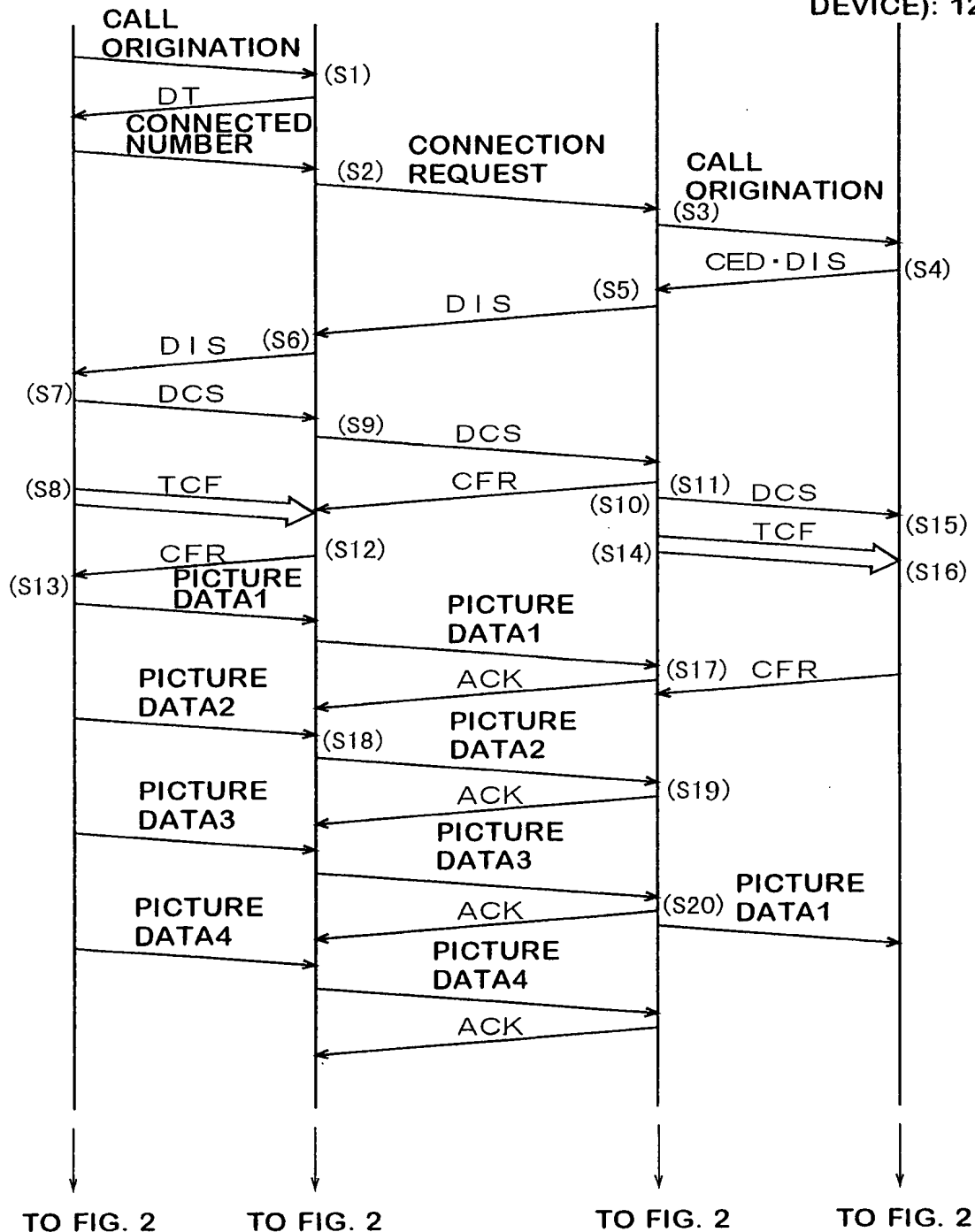


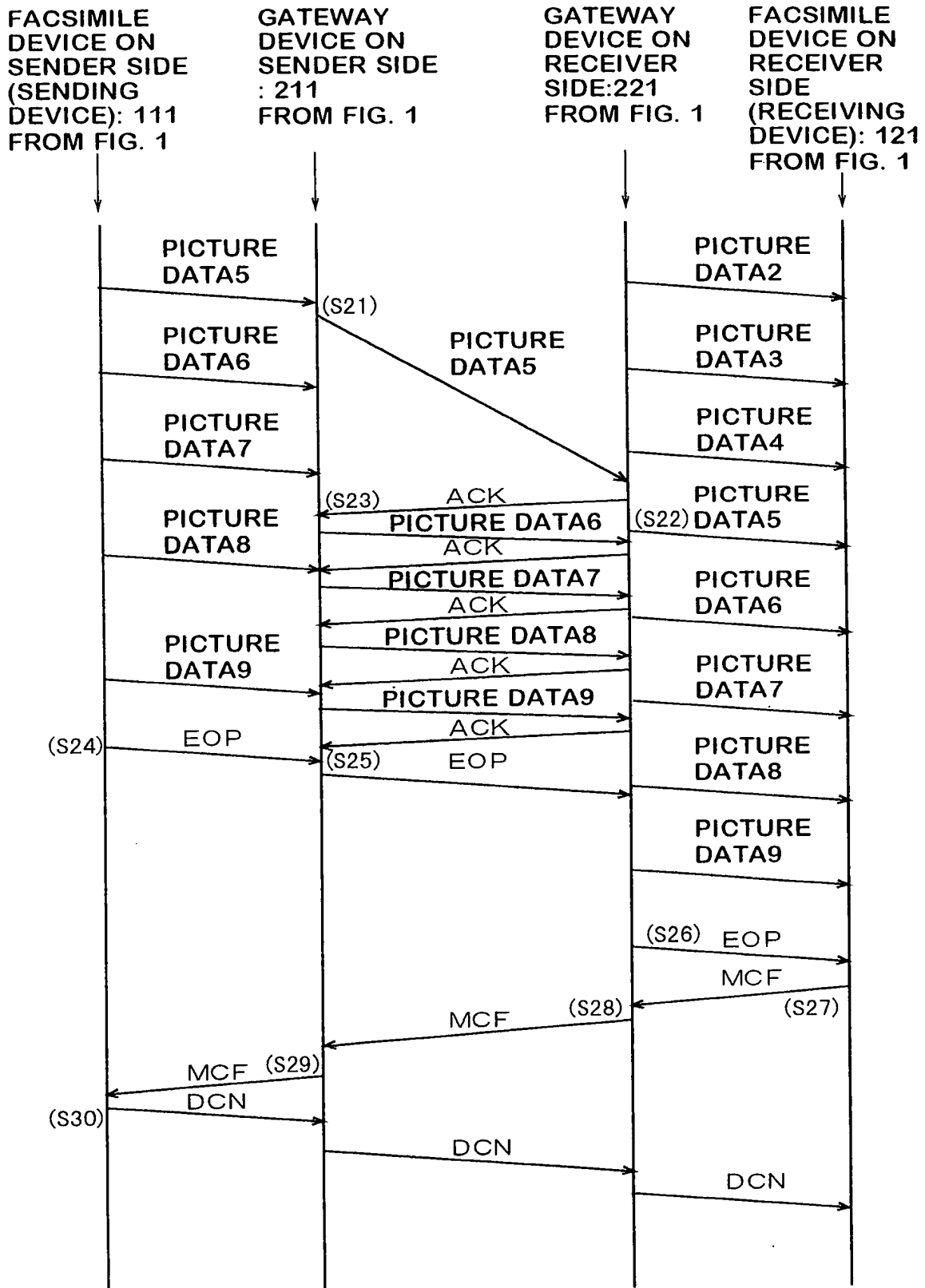
Fig.2

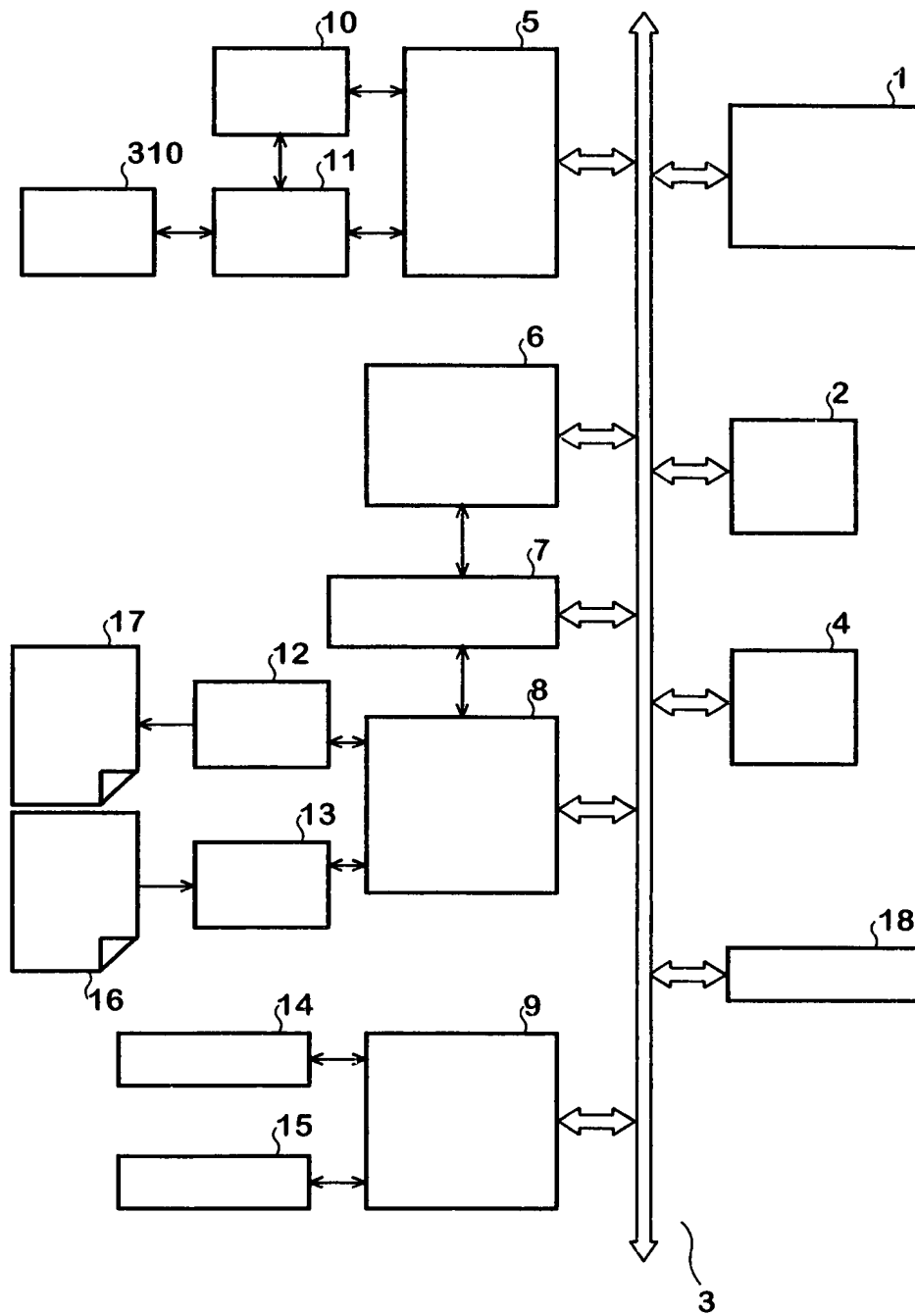
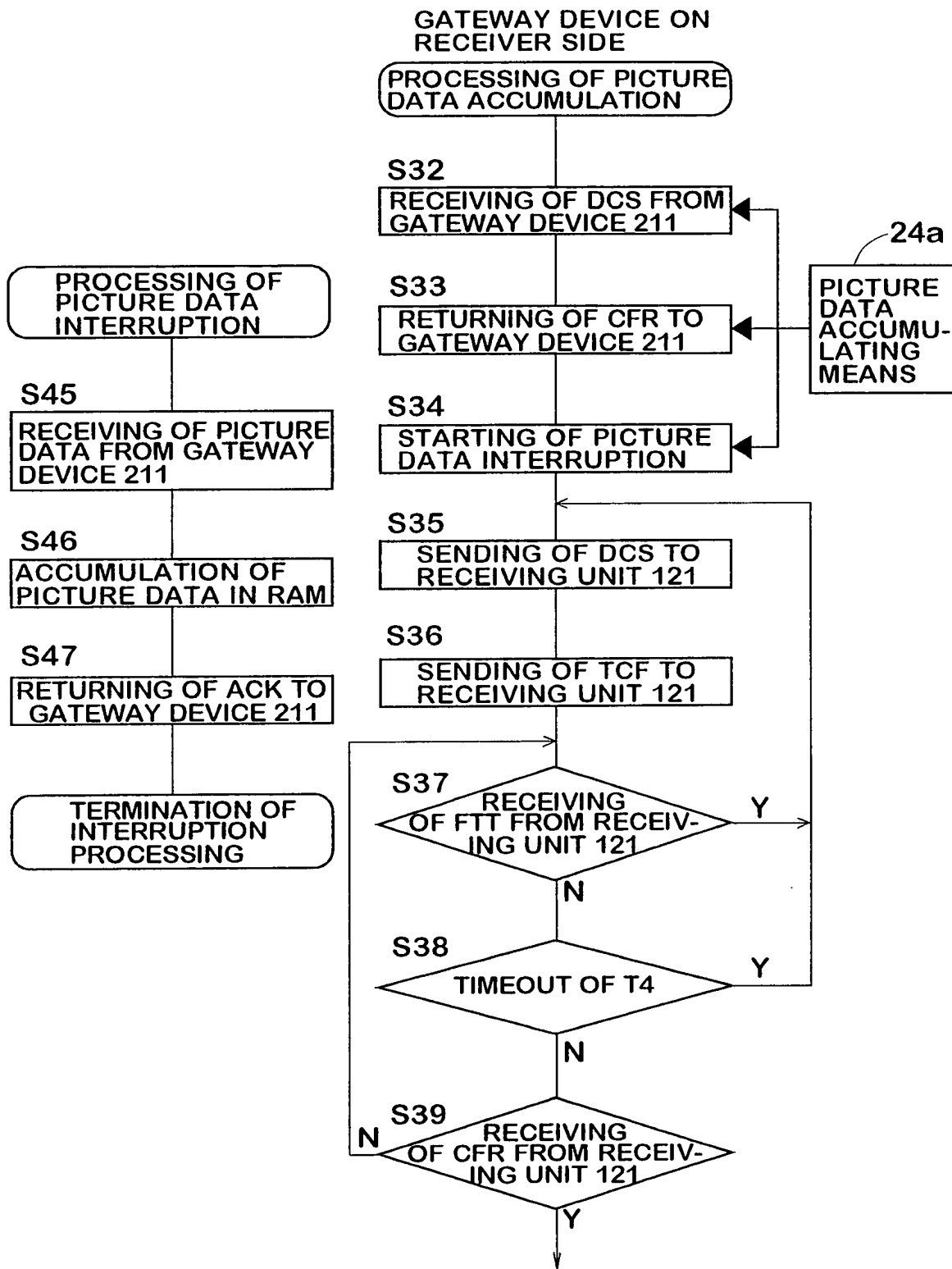
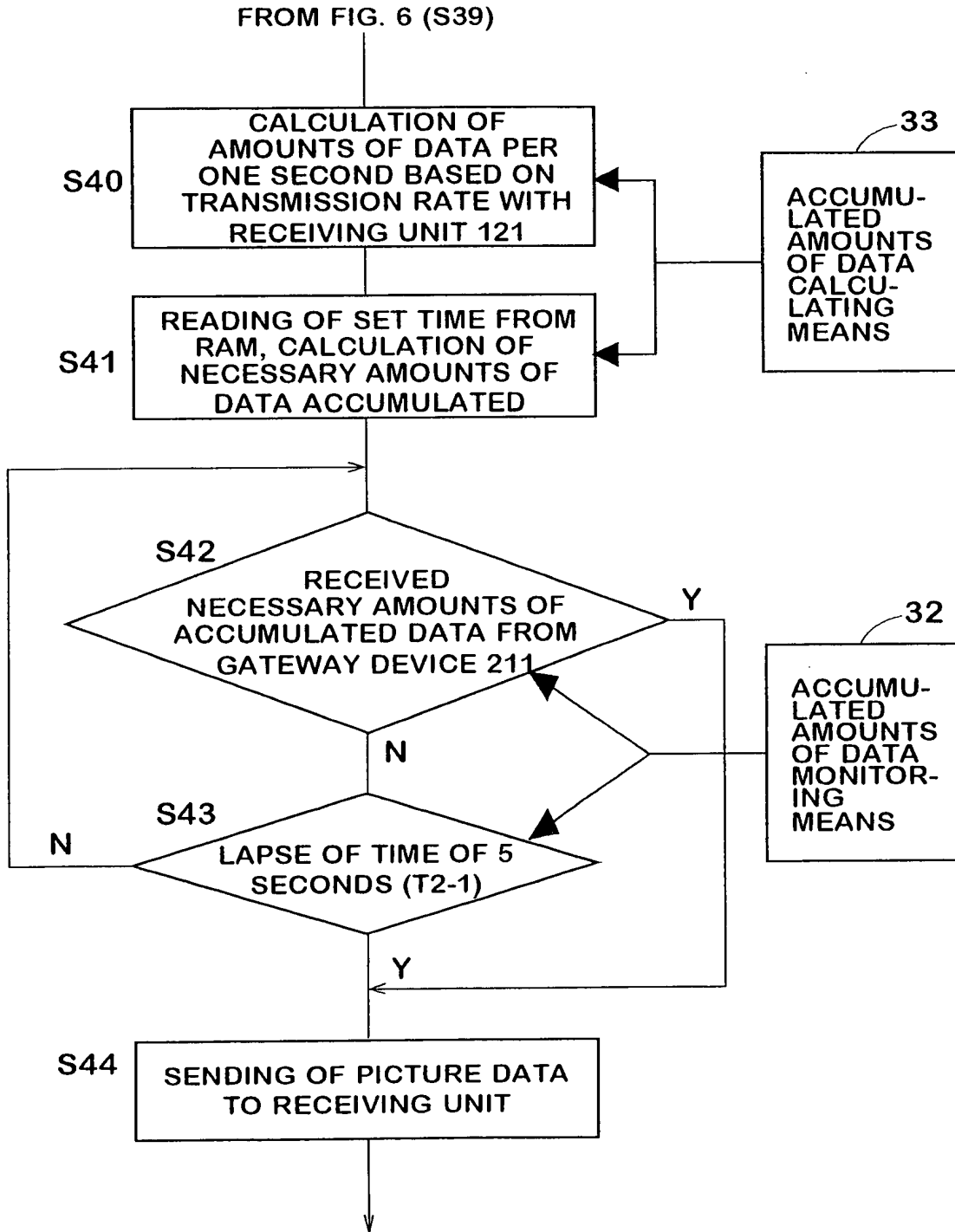
Fig.4

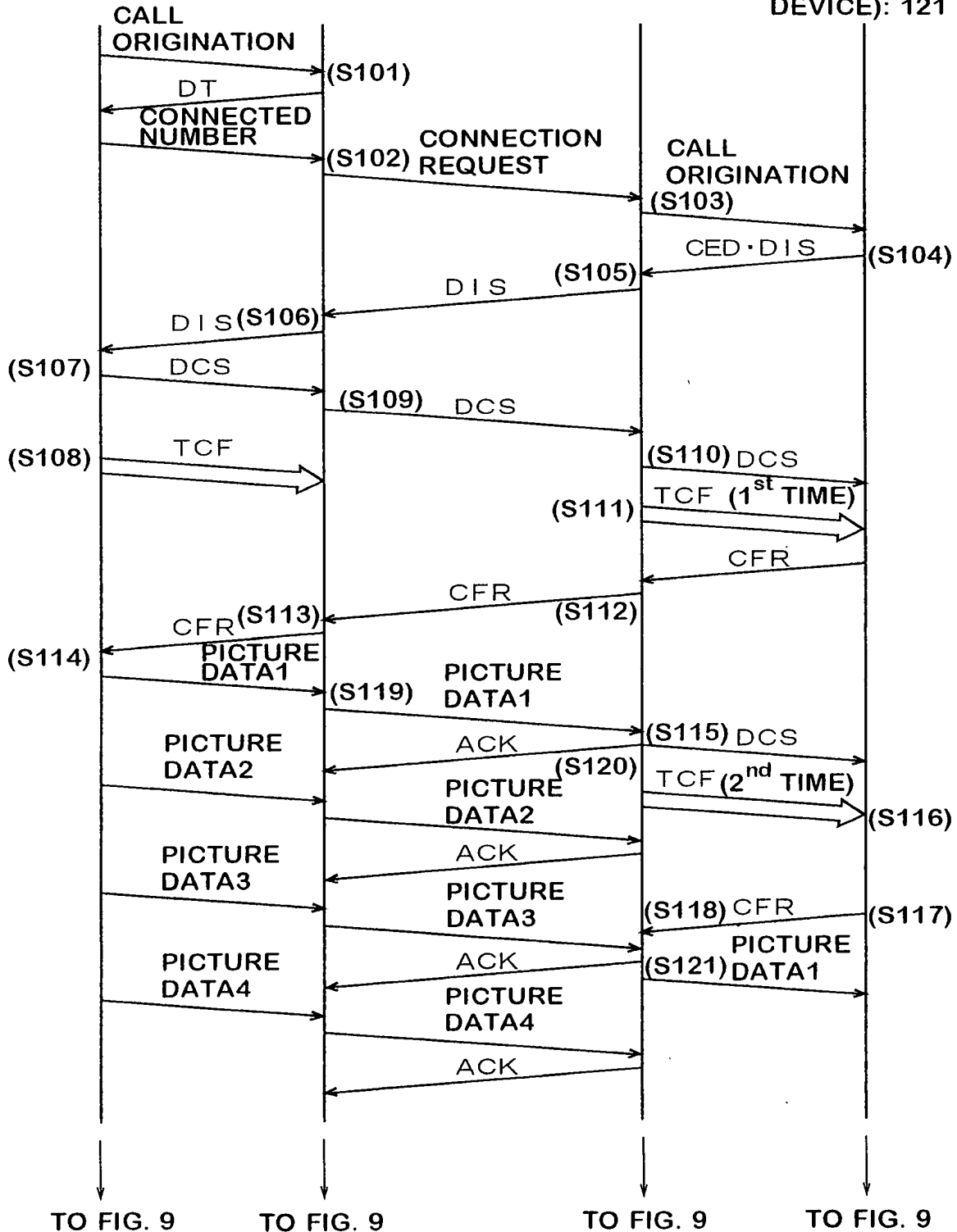
Fig.6



TO FIG. 7

Fig. 7

FACSIMILE DEVICE ON SENDER SIDE (SENDING DEVICE): 111 CALL	GATEWAY DEVICE ON SENDER SIDE : 211	GATEWAY DEVICE ON RECEIVER SIDE:221	FACSIMILE DEVICE ON RECEIVER SIDE (RECEIVING DEVICE): 121



FACSIMILE
DEVICE ON
SENDER SIDE
(SENDING
DEVICE): 111
FROM FIG. 8

GATEWAY
DEVICE ON
SENDER SIDE
: 211
FROM FIG. 8

GATEWAY
DEVICE ON
RECEIVER
SIDE: 221
FROM FIG. 8

FACSIMILE
DEVICE ON
RECEIVER
SIDE
(RECEIVING
DEVICE): 121
FROM FIG. 8

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sequenceDiagram
    participant S111 as FACSIMILE DEVICE ON SENDER SIDE (SENDING DEVICE): 111 FROM FIG. 8
    participant S211 as GATEWAY DEVICE ON SENDER SIDE : 211 FROM FIG. 8
    participant S221 as GATEWAY DEVICE ON RECEIVER SIDE: 221 FROM FIG. 8
    participant S121 as FACSIMILE DEVICE ON RECEIVER SIDE (RECEIVING DEVICE): 121 FROM FIG. 8

    S111->>S211: PICTURE DATA5
    S111->>S211: PICTURE DATA6
    S111->>S211: PICTURE DATA7
    S111->>S211: PICTURE DATA8
    S111->>S211: PICTURE DATA9
    S111->>S211: EOP
    S111->>S211: MCF (S130)
    S111->>S211: DCN
    S211->>S221: PICTURE DATA5
    S211->>S221: ACK
    S211->>S221: PICTURE DATA6
    S211->>S221: ACK
    S211->>S221: PICTURE DATA7
    S211->>S221: ACK
    S211->>S221: PICTURE DATA8
    S211->>S221: ACK
    S211->>S221: PICTURE DATA9
    S211->>S221: ACK
    S211->>S221: EOP
    S211->>S221: MCF
    S221->>S121: PICTURE DATA2
    S221->>S121: PICTURE DATA3
    S221->>S121: PICTURE DATA4
    S221->>S121: PICTURE DATA5
    S221->>S121: PICTURE DATA6
    S221->>S121: PICTURE DATA7
    S221->>S121: PICTURE DATA8
    S221->>S121: PICTURE DATA9
    S221->>S121: (S127)EOP
    S221->>S121: MCF
    S221->>S121: (S128)
    S221->>S121: DCN
```

Fig. 10A

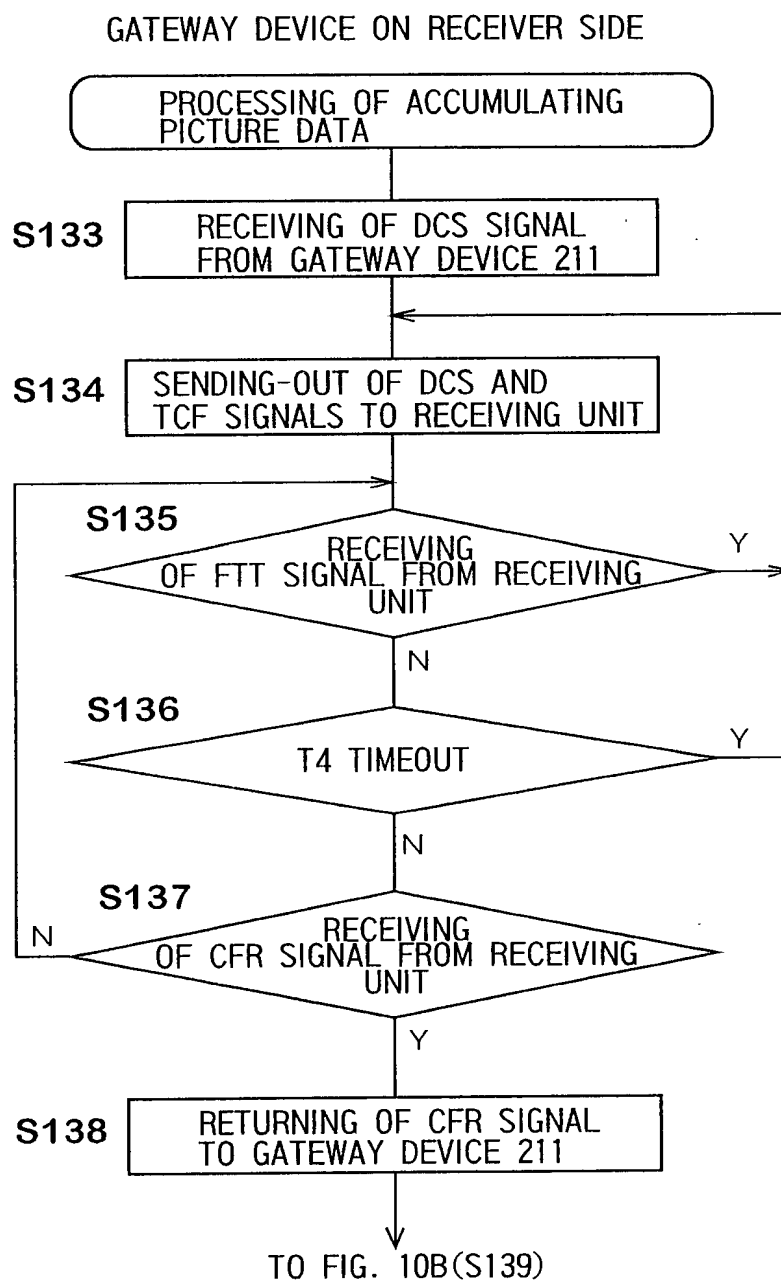


Fig. 10B

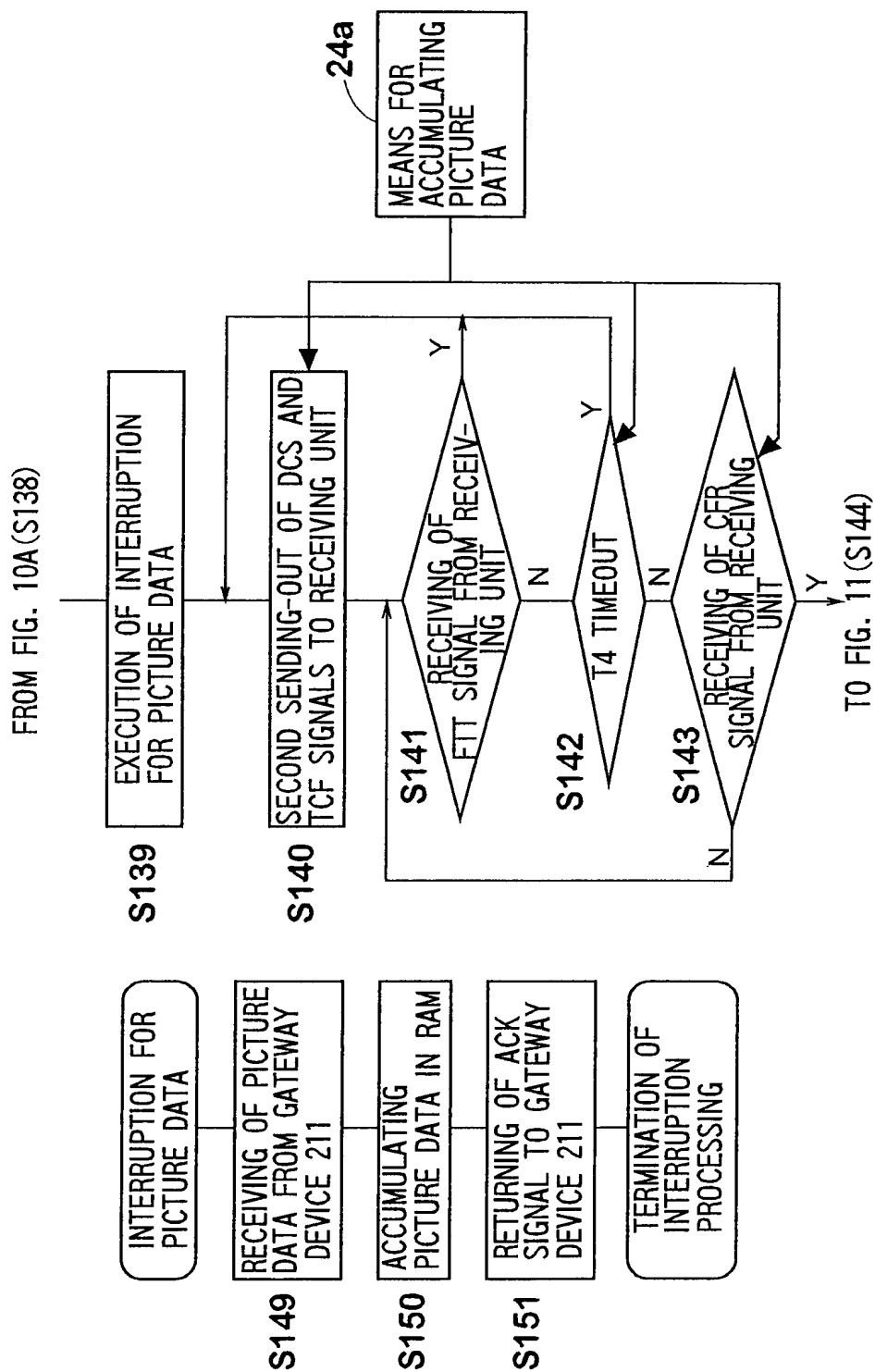
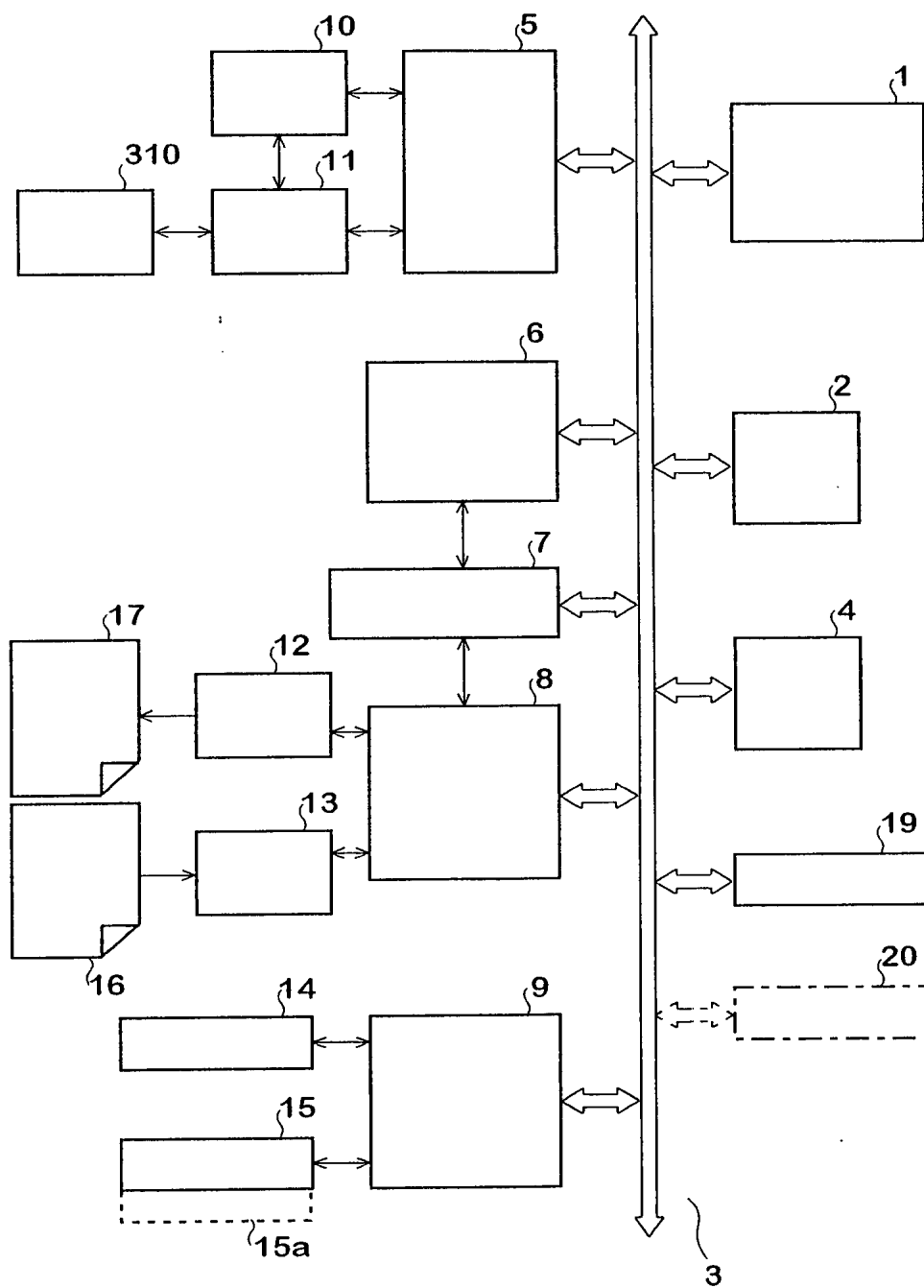


Fig. 12



008280" 25CTM960

The diagram illustrates a system architecture with a central vertical bus (23) that facilitates communication with multiple components. The components are organized as follows:

- Left Side Components:**
 - Component 29 is connected to component 30 via a bidirectional arrow.
 - Component 30 is connected to component 31 via a bidirectional arrow.
 - Component 31 is connected to component 310 via a bidirectional arrow.
 - Component 26 is connected to component 27 via a bidirectional arrow.
 - Component 27 is connected to component 28 via a bidirectional arrow.
 - Component 28 is connected to component 400 via a bidirectional arrow.
- Right Side Components:**
 - Component 21 is connected to the bus (23) via a bidirectional arrow.
 - Component 22 is connected to the bus (23) via a bidirectional arrow.
 - Component 24 is connected to the bus (23) via a bidirectional arrow.
 - Component 35 is connected to the bus (23) via a bidirectional arrow.
 - Component 36 is connected to the bus (23) via a bidirectional arrow.
 - Component 37 is connected to the bus (23) via a bidirectional arrow.
- Central Bus:** A vertical line (23) with arrows at both ends, representing a communication bus that connects to components 21, 22, 24, 35, 36, and 37.

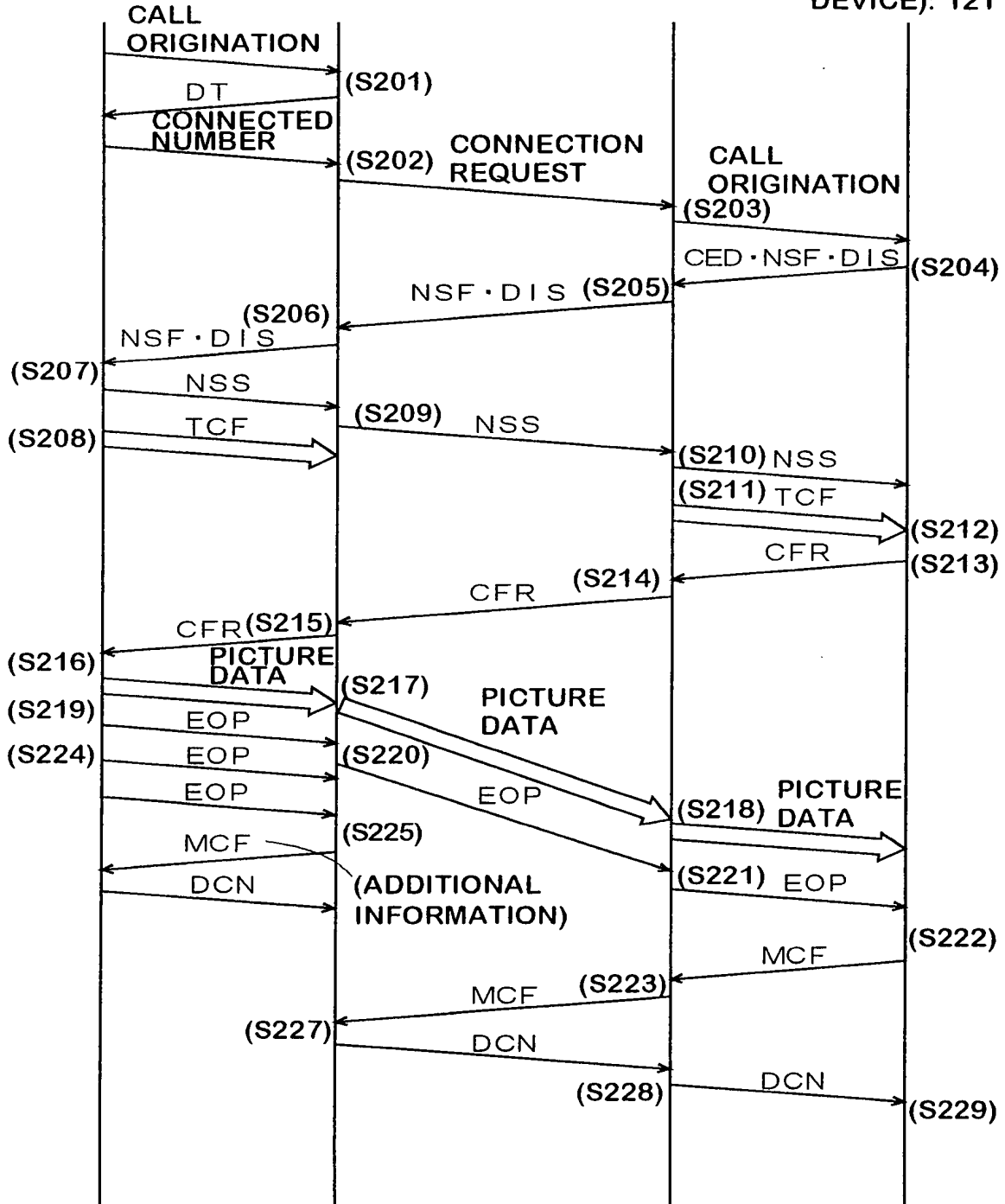
Fig.14

FACSIMILE
DEVICE ON
SENDER SIDE
(SENDING
DEVICE): 111

GATEWAY
DEVICE ON
SENDER SIDE
: 211

GATEWAY
DEVICE ON
RECEIVER
SIDE:221

FACSIMILE
DEVICE ON
RECEIVER
SIDE
(RECEIVING
DEVICE): 121



008280" 29CT5960

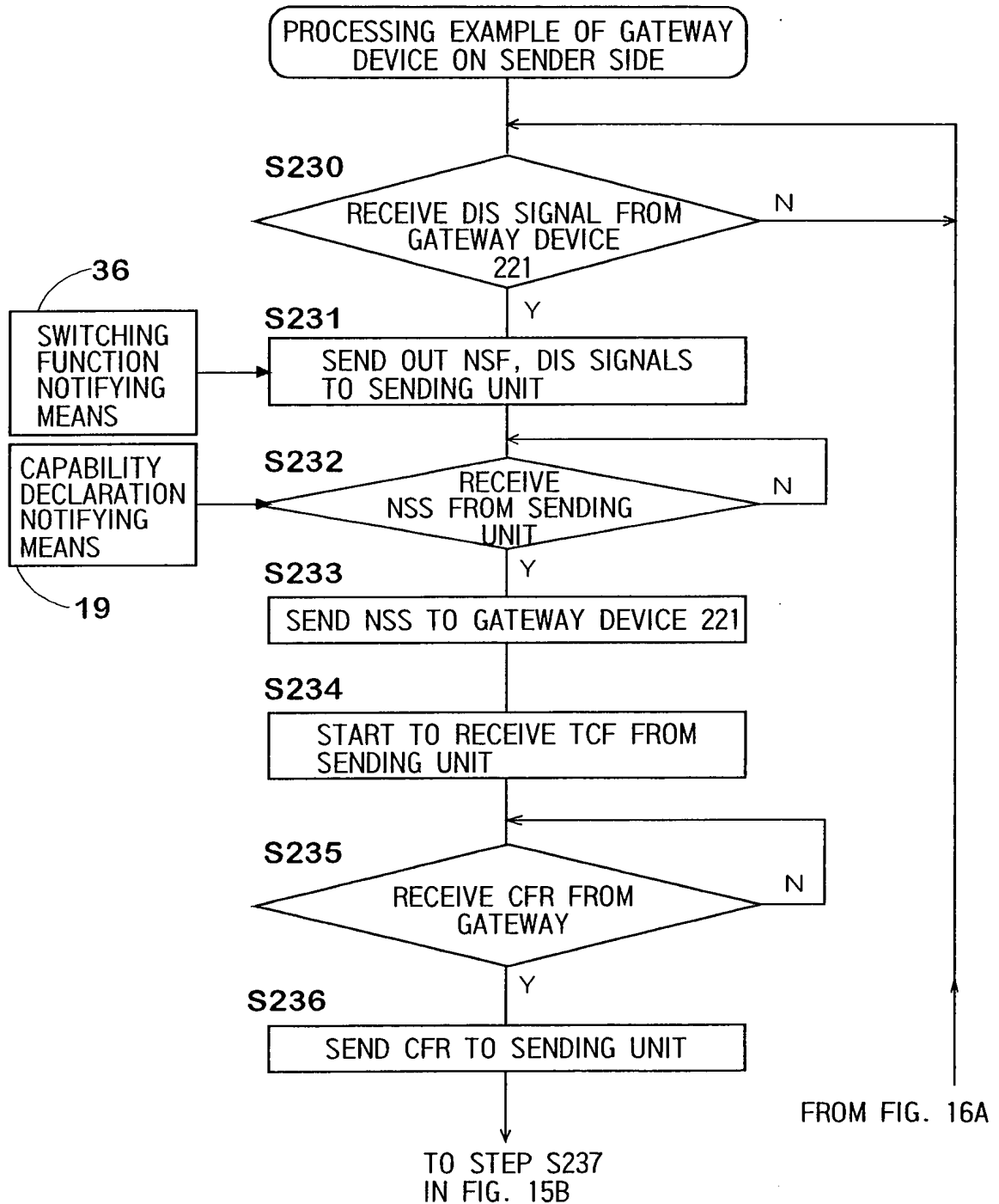
Fig. 15A

Fig.15B

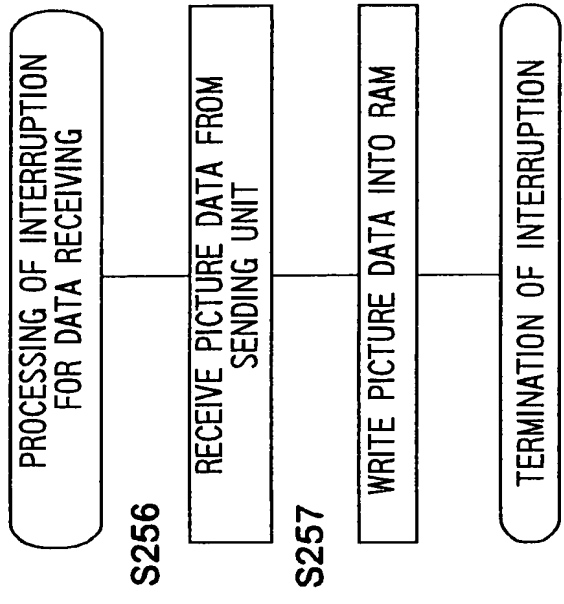
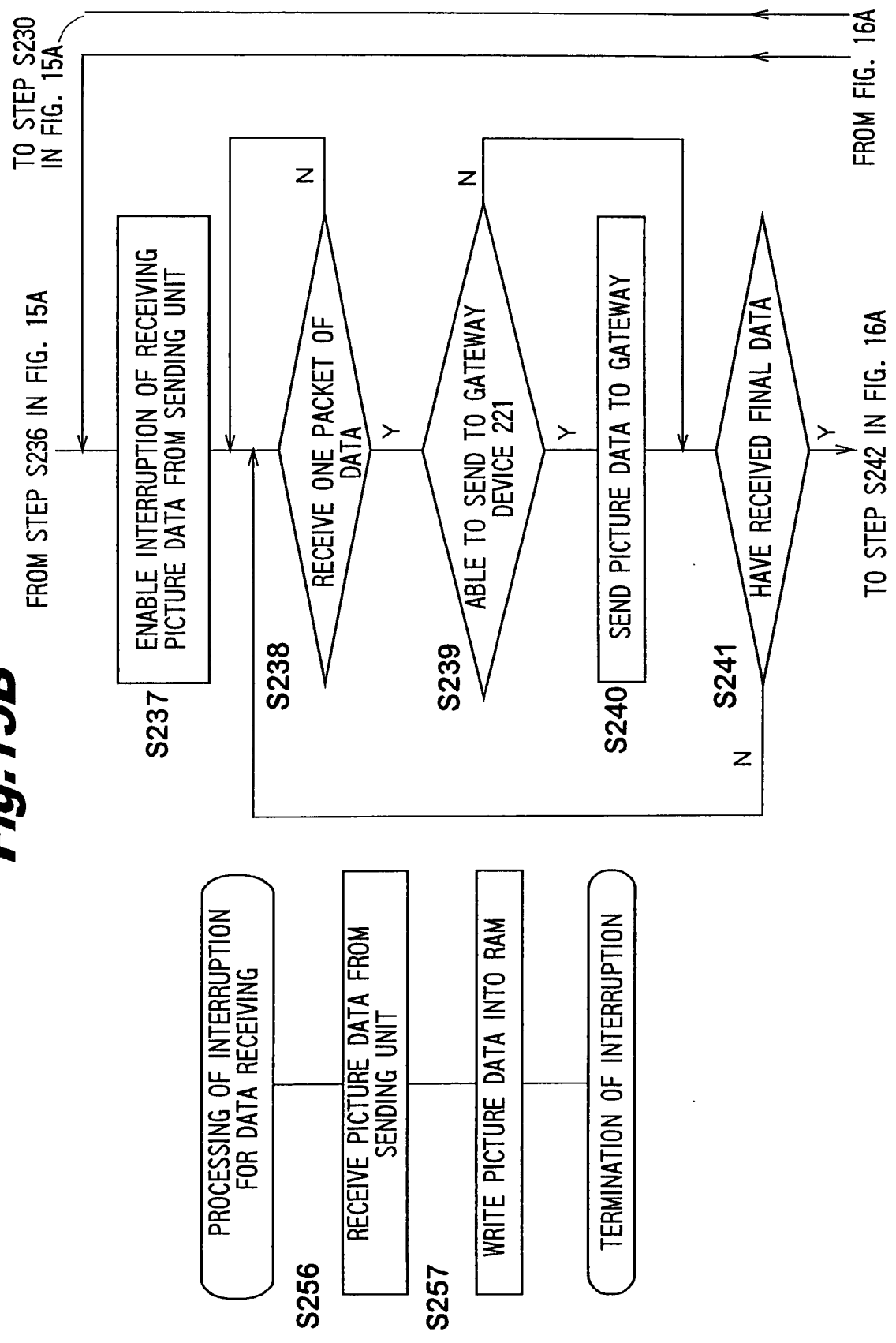


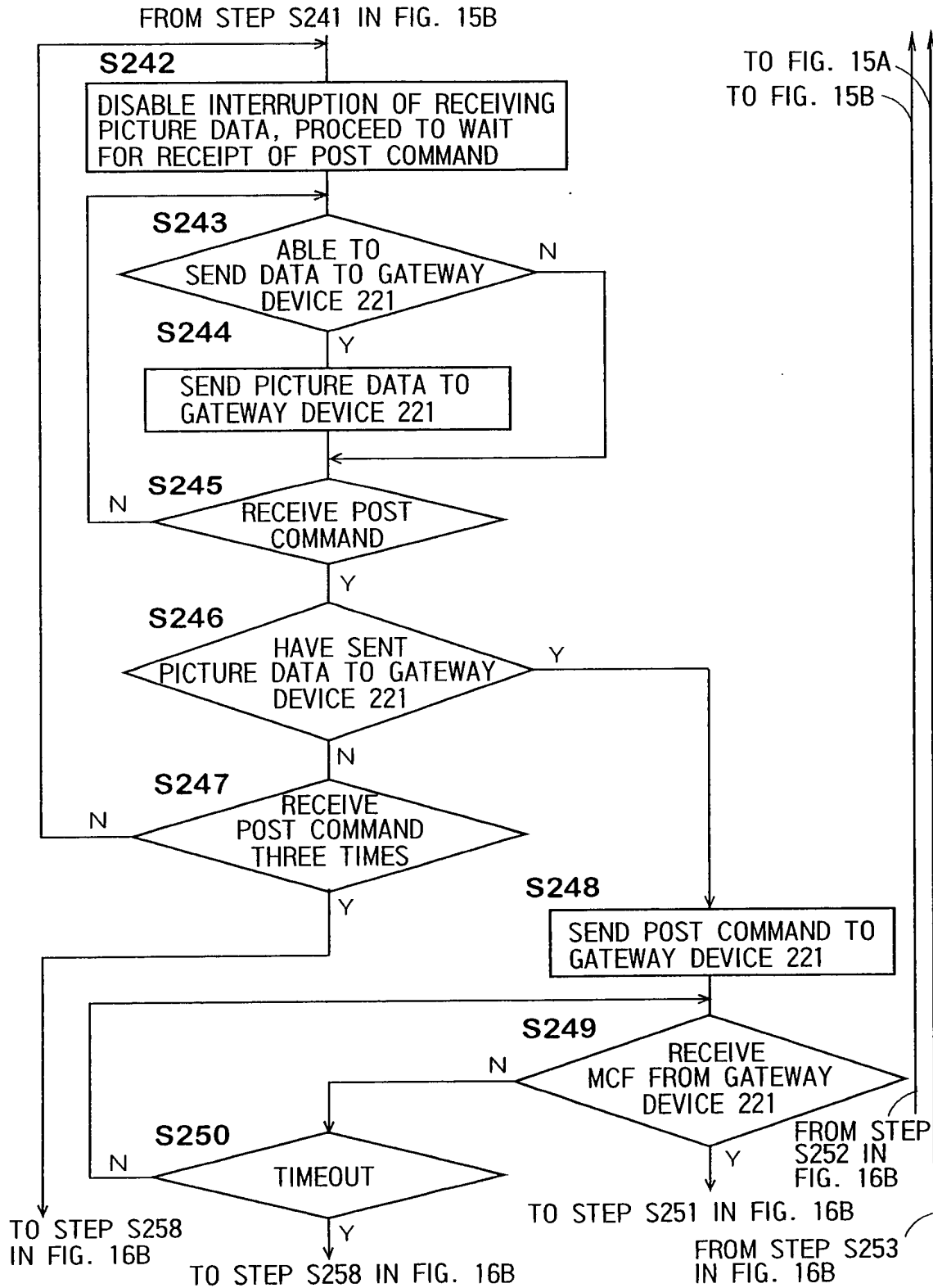
Fig. 16A

Fig. 16B

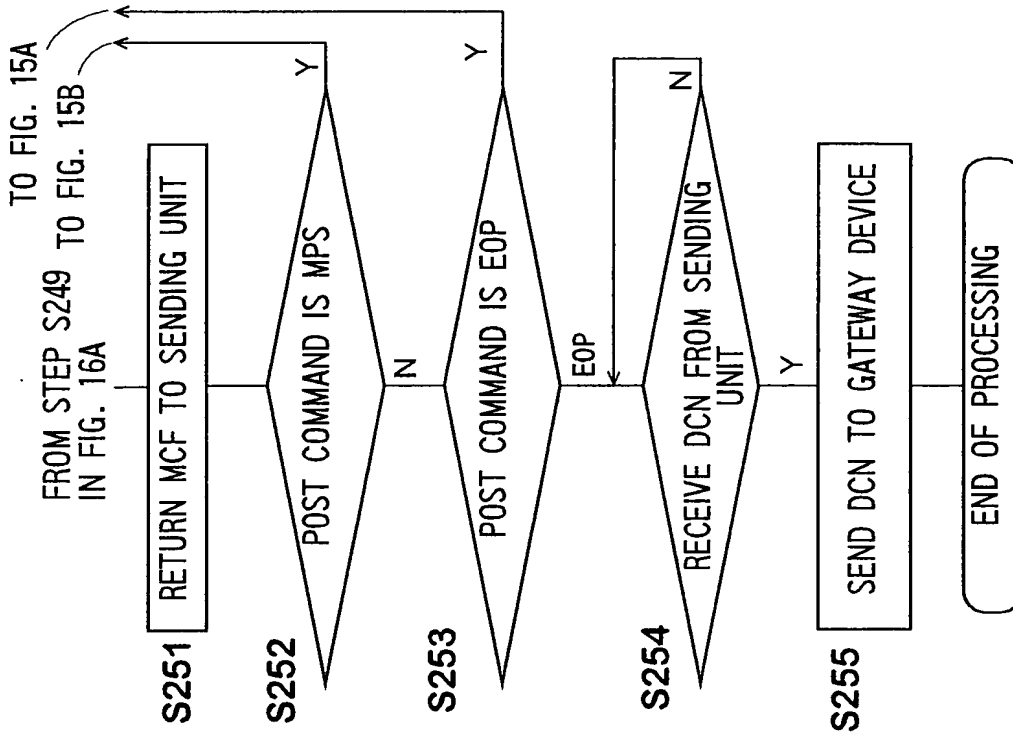
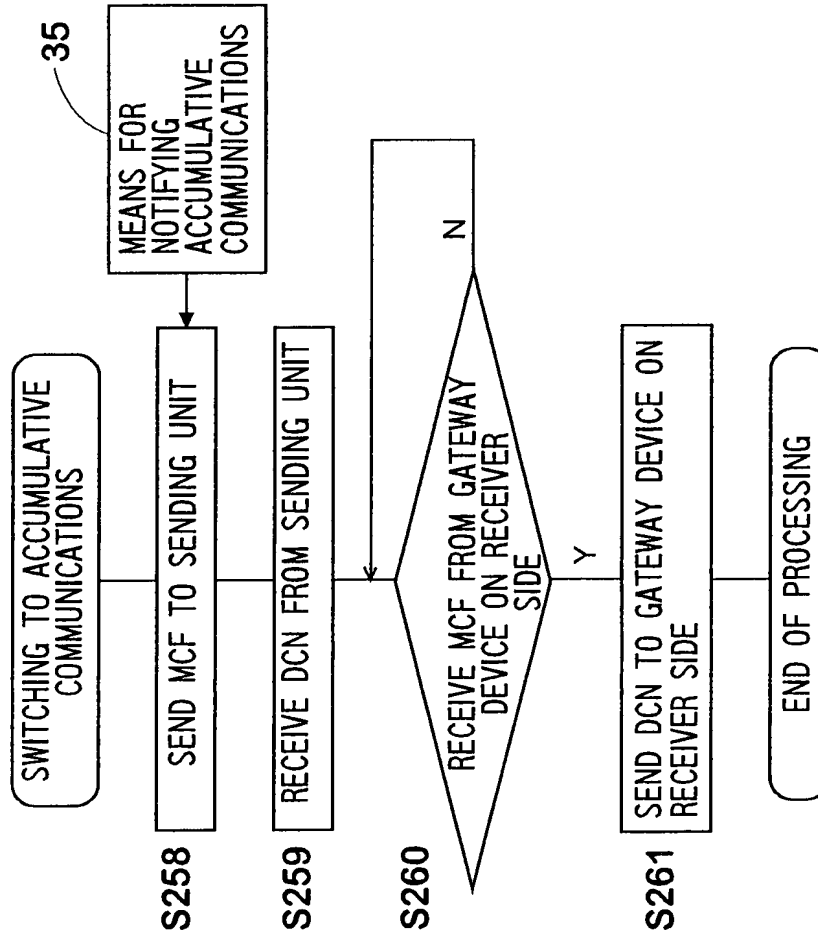


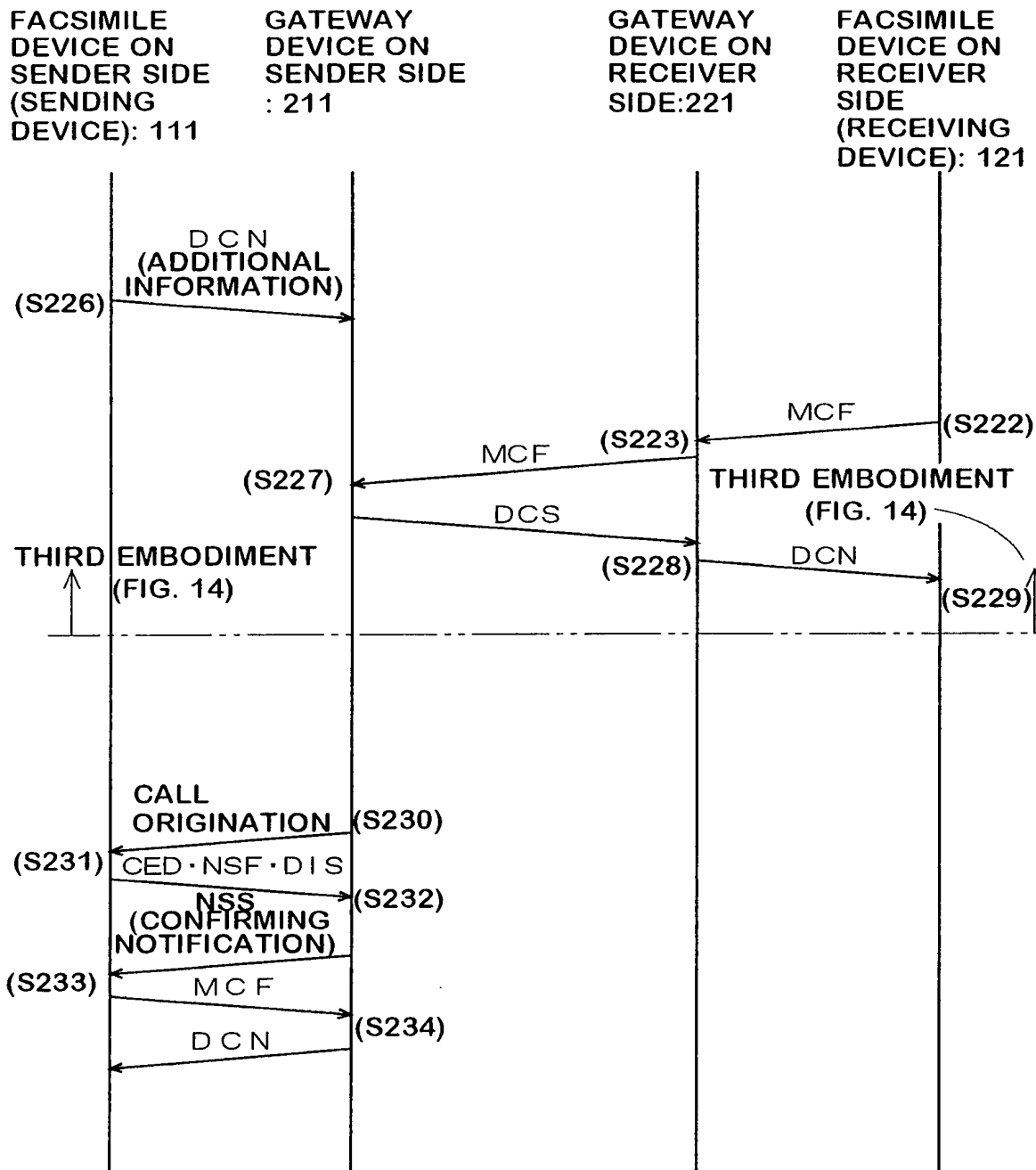
Fig.17

Fig.18

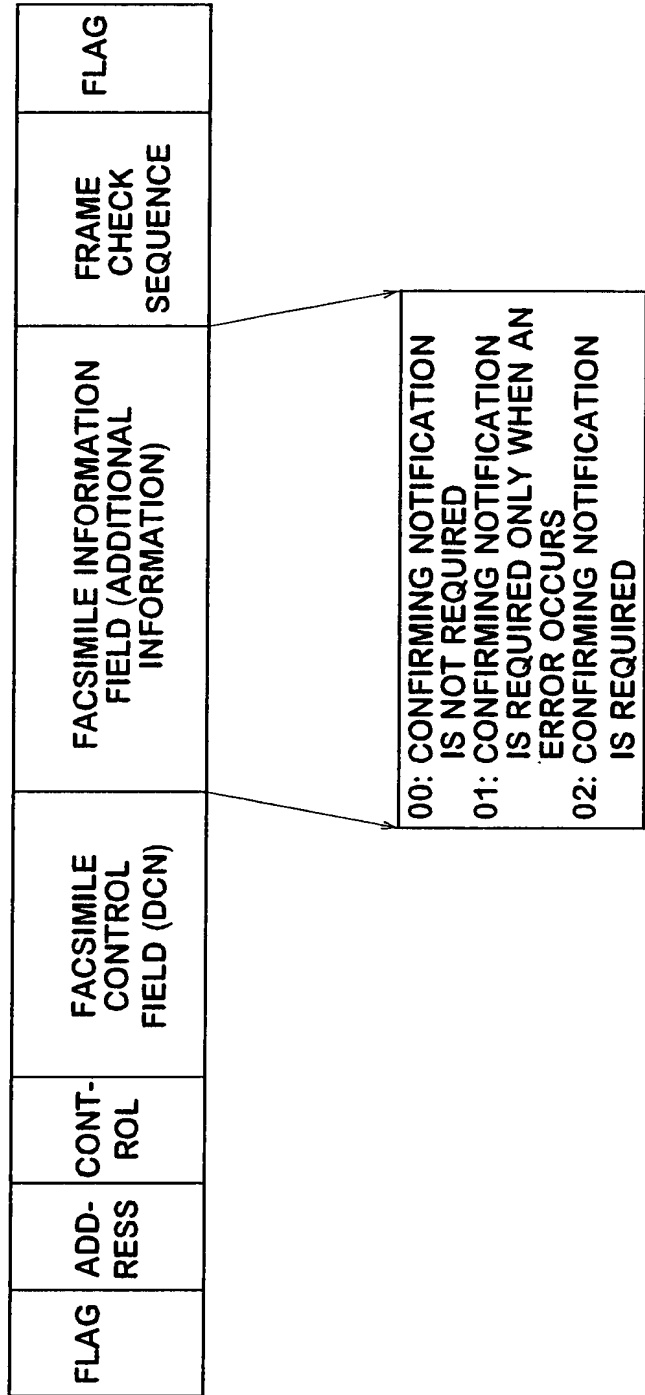
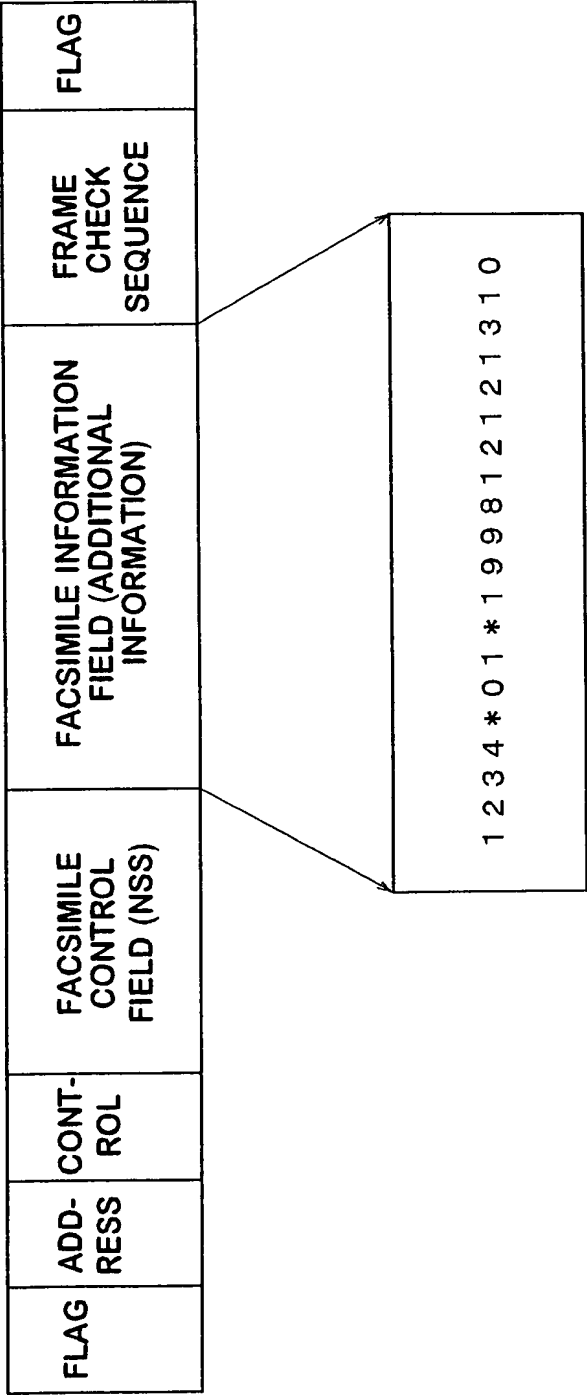


Fig.19



1234: ACCEPTANCE NUMBER
01: COMMUNICATION RESULTS (00 IN THE CASE OF NG)
199812121310: COMMUNICATION END TIME
(13:10, DECEMBER 12, 1998)

FROM STEP S258 IN FIG. 16B

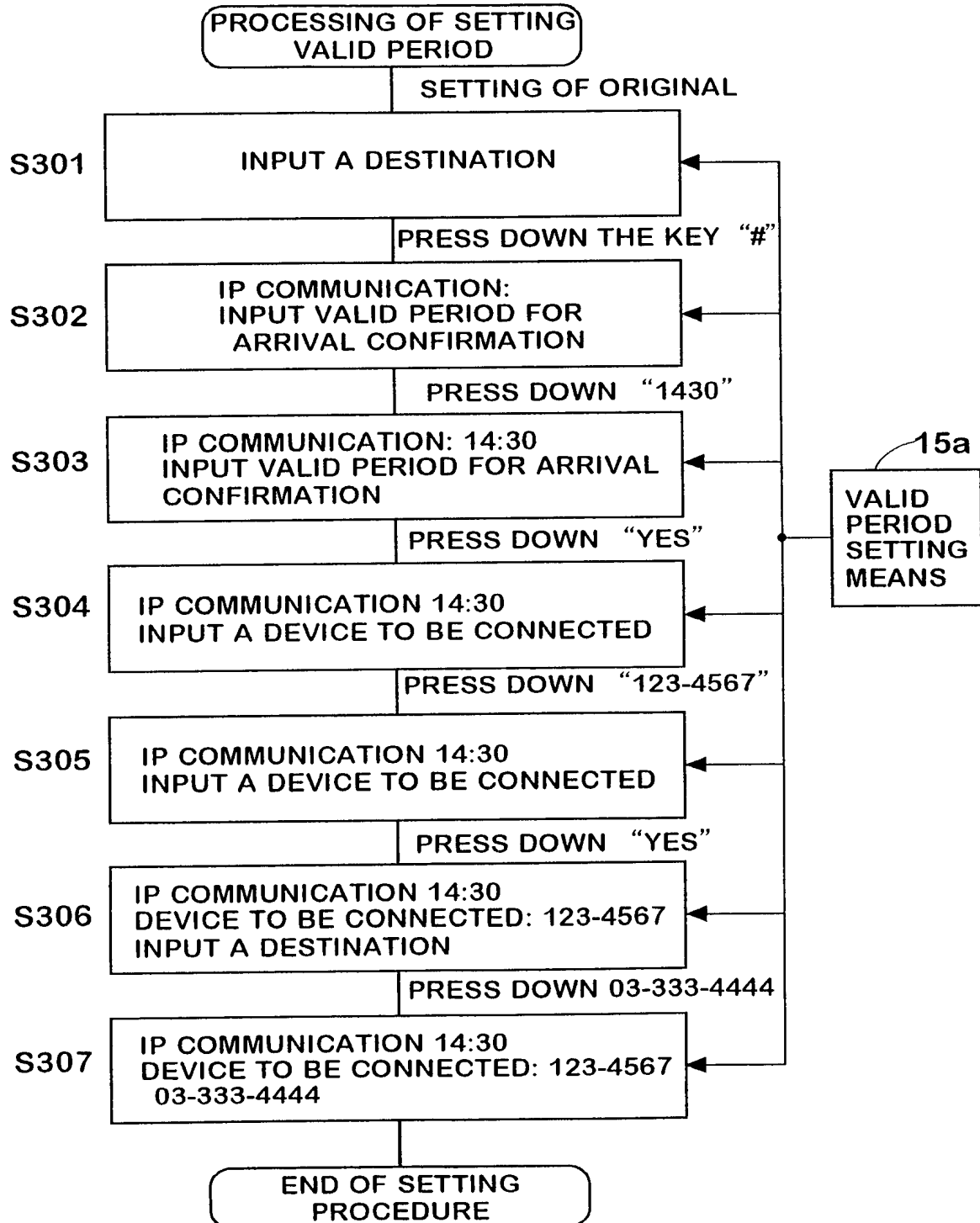
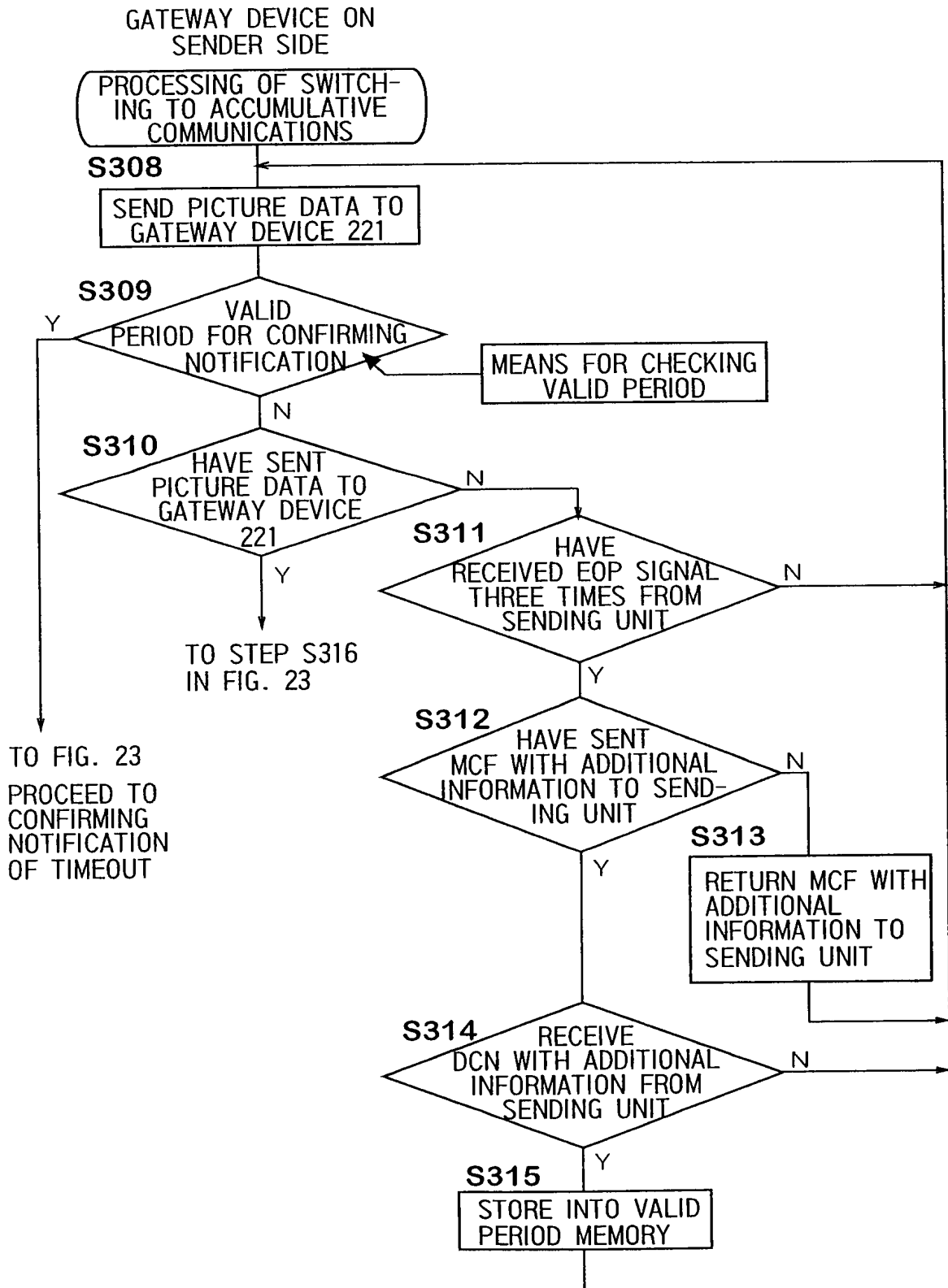
Fig.21

Fig.22

005280" 2875950

Fig. 23

